

REMARKS

In response to the Office Action dated February 21, 2008 Applicant respectfully requests reconsideration based on the above claim amendments and the following remarks. Applicant respectfully submits that the claims as presented are in condition for allowance.

Claims 1-14 are pending in the present Application. Claims 1, 7 and 12 are amended, and Claims 15 and 16 are added, leaving Claims 1-16 for consideration upon entry of the present amendments and the following remarks.

Support for the amendments to claims is at least found in the specification, the figures, and the claims as originally filed. Particularly, support for amended Claims 1, 7 and 12, and new Claims 15 and 16 is at least found in the originally specification at page 7, lines 14-17, page 8, lines 3-14, page 10, line 20 through page 11, line 10, page 12, line 8 through page 13, line 6 and page 13, lines 16-23, and Figures 3 and 4.

No new matter has been introduced by these amendments. Reconsideration and allowance of the claims are respectfully requested in view of the above amendments and the following remarks.

Claim Rejections Under 35 U.S.C. §102

To anticipate a claim under 35 U.S.C. § 102, a single source must contain all of the elements of the claim. *Lewmar Marine Inc. v. Barient, Inc.*, 827 F.2d 744, 747, 3 U.S.P.Q.2d 1766, 1768 (Fed. Cir. 1987), *cert. denied*, 484 U.S. 1007 (1988). Moreover, the single source must disclose all of the claimed elements “arranged as in the claim.” *Structural Rubber Prods. Co. v. Park Rubber Co.*, 749 F.2d 707, 716, 223 U.S.P.Q. 1264, 1271 (Fed. Cir. 1984).

Claims 12 and 13 are rejected under 35 U.S.C. § 102(b) as being anticipated by Kawaguchi et al., U.S. Patent No. 5,592,199 (hereinafter “Kawaguchi”). Applicant respectfully traverses the rejections.

Amended Claim 12 recites, *inter alia*:

“gate lines disposed on a second substrate and opposing a common electrode formed on a first substrate facing the second substrate, the gate lines receiving a gate driving signal; and an output instruction signal line disposed on the second substrate and opposing the common electrode such that the output instruction signal line has a capacitive load.”

On page 2 of the Office action, electrode terminals 3, connector 8 and bus line 73 in Figure 1 of Kawaguchi are considered as disclosing the “gate lines,” the “timing controller” and the “output instruction signal line,” respectively, of the claimed invention.

Kawaguchi discloses liquid crystal sandwiched between a pair of substrates 1 and 2, where the electrode terminals 3 are disposed on *a peripheral portion* of the glass substrate 2 and lead to pixels (not shown) inside the panel. (Figures 1-3 and Col. 18, lines 31-48.) The bus line 73 is provided at a layer below the electrode terminals 3 and extends along *the peripheral portion* of the glass substrate 2. (*Id.*) As clearly disclosed and illustrated by Kawaguchi, the bus line 73 being disposed on the substrate 2 in no way “opposes” the substrate 1, especially the substrate 1 including a common electrode such that the bus line 73 has a capacitive load. Therefore, Kawaguchi does not disclose gate lines disposed on a second substrate and opposing a common electrode formed on a first substrate facing the second substrate, the gate lines receiving a gate driving signal; and an output instruction signal line disposed on the second substrate and opposing the common electrode such that the output instruction signal line has a capacitive load of amended Claim 12.

Additionally, referring to Figure 3 of Kawaguchi, a bus line 73 is covered by an anisotropic conductive material 95, and a flexible wiring board 4 is disposed on the anisotropic conductive material 95 so that the flexible wiring board 4 is adhered to a lower substrate 2. Accordingly, the bus line 73 of Kawaguchi is not overlapped with an upper substrate including a common electrode. Therefore, Kawaguchi further does not disclose an output instruction signal line disposed on the second substrate and opposing the common electrode such that the output instruction signal line has a capacitive load of amended Claim 12.

Thus, Kawaguchi *fails to disclose* all of the limitations of amend Claim 12. Accordingly, Kawaguchi does not anticipate Claim 12. Applicant respectfully submits that Claim 12 is not further rejected or objected and is therefore allowable. Claim 13 depends from Claim 12 and thus inherits all of the limitations of amended Claim 12 and is correspondingly allowable. Entry of the claim amendments, reconsideration, withdrawal of the relevant §102 rejections and allowance of Claims 12 and 13 are respectfully requested.

Claim Rejections under 35 U.S.C. §103

The Examiner has rejected Claims 1 – 11 and 14 under 35 U.S.C. § 103(a) as being unpatentable over Kawaguchi in view of Kubota et al., U.S. Patent No. 6,791,526 (hereinafter “Kubota”). Applicant respectfully traverses the rejections.

Independent Claims 7 and 12 are amended similarly to independent Claim 1. Claims 2-6, 8-11 and 14 variously depend from Claims 1, 7 and 12, and inherit all of the limitations of amended Claims 1, 7 and 12, respectively. For all the reasons discussed above, Kawaguchi *does not teach or suggest* an output instruction signal line disposed on the second substrate and opposing the common electrode such that the output instruction signal line has a capacitive load, the output instruction signal line transmitting an output instruction signal of similarly amended Claims 1, 7 and 12, and of Claims 2-6, 8-11 and 14 as depending upon Claims 1, 7 and 12.

Regarding Claims 1 and 7, it is conceded in the instant Office action that Kawaguchi does not teach or suggest that the timing of output of image data is according to a delay of the gate driving signal. Kubota is relied upon as allegedly disclosing that a timing of an output of image data is according to a delay of a gate driving signal. (See Figure 18). In addition, Kubota is relied upon as disclosing that specific gate lines are energized with the timing that corresponds to control signals from timing control circuits 5 and that thin film transistors of pixel cells connected to these gate lines are switched on at a same time. (See Col. 1, lines 60-65).

Applicant respectfully submits that Kubota also *does not teach or suggest* an output instruction signal line disposed on the second substrate and opposing the common electrode such that the output instruction signal line has a capacitive load, the output instruction signal line transmitting an output instruction signal of similarly amended Claims 1, 7 and 12, and of Claims 2-6, 8-11 and 14 as depending upon Claims 1, 7 and 12, and does not remedy the deficiencies of Kawaguchi.

Thus, Kawaguchi and Kubota, alone or in combination, *fail to disclose all of the limitations* of Claims 1-11 and 14. Accordingly, *prima facie* obviousness does not exist regarding 1-11 and 14 with respect to Kawaguchi and Kubota. Applicant respectfully submits that Claims 1-11 and 14 are not further rejected or objected and are therefore allowable. Entry of the claim amendments, reconsideration, withdrawal of the relevant §103 rejections and allowance of Claims 1–11 and 14 are respectfully requested.

Conclusion

All of the objections and rejections are herein overcome. In view of the foregoing, it is respectfully submitted that the instant application is in condition for allowance. No new matter is added by way of the present Amendments and Remarks, as support is found throughout the original filed specification, claims and drawings. Prompt issuance of Notice of Allowance is respectfully requested.

The Examiner is invited to contact Applicant's attorney at the below listed phone number regarding this response or otherwise concerning the present application.

Applicant hereby petitions for any necessary extension of time required under 37 C.F.R. 1.136(a) or 1.136(b) which may be required for entry and consideration of the present Reply.

If there are any charges due with respect to this Amendment or otherwise, please charge them to Deposit Account No. 06-1130 maintained by Applicant's attorneys.

Respectfully submitted,

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